Src1: <https://www.iitg.ac.in/cseweb/vlab/Digital-System-Lab/fa_mux.php?id=10#:~:text=Full%20Adder%20using%204%20to%201%20Multiplexer%3A&text=A%204%20to%201%20line,S%2CCout%20are%20the%20outputs>.

Src2: [~~https://www.geeksforgeeks.org/1-bit-full-adder-using-multiplexer/#:~:text=Multiplexer%20and%20Full%20adder%20are,and%20Carry%20as%20an%20output~~](https://www.geeksforgeeks.org/1-bit-full-adder-using-multiplexer/#:~:text=Multiplexer%20and%20Full%20adder%20are,and%20Carry%20as%20an%20output)~~.~~

~~Src3: https://youtu.be/BkyaHwt6l84~~